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PATENT APPLICATION

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Inventor(s):

D. Mitchel Hanks

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**Examiner: Unknown** 

**Group Art Unit:** 

**Title: Error Correction Circuit** 

**Commissioner For Patents** PO Box 1450 Alexandria, VA 22313-1450

## STATUS INQUIRY

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More	e than22 months have passed since:			
$\boxtimes$	NEW APPLICATIONS The filing of this application on March 9, 2004 No communication has been received from the Patent and Trademark Office indicating action on this application.			
	AMENDED APPLICATIONS The filing of a response on No comm Trademark Office.	No communication has been received from the Patent and		
	APPEALED APPLICATION The Appeal Brief was filed on			
An Examiner's Answer was mailed on				
A Reply Brief to the Examiner's Answer was submitted on				
A reply bliche the Examiner of historic mae dublinated on				
	ALLOWED APPLICATIONS The mailing of FORM POL-85 and/or Examiner's Amendment on			
Kindly advise the undersigned of the present status of this application.				
	I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:  Commissioner for Patents, Alexandria, VA 22313-1450		Respectfully submitted,  D. Mitchel Hanks	
	Date of Deposit: January 30, 2006	By //im	rushy Ville	
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	I hereby certify that this paper is being transmitted to the Patent and Trademark Office facsimile number (571) 273-8300.  Date of facsimile:	Timothy F. Myer	42,919	
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	( ) ( )		•	

Rev 10/05 (Status)